

X2A		
A2		GND_5
A3	VCC_IO_1	VCC_BUCK4_1
A4	VCC_IO_2	VCC_BUCK4_2
A5	PA11_FDCAN1_RX	GND_6
A6	VBUS_SW_1	USBH_HS1_DP
A7	GND_1	GND_7
A8	VBUS_OTG_1	USB_OTG_HS_VBUS
A9	PF9_QSPI_BK1_I01	USB_OTG_HS_DP
A10	PF8_QSPI_BK1_I00	PF6_QSPI_BK1_I03
A11	VCC_IN_SV_1	VCC_IN_SV_5
A12	VCC_IN_SV_2	VCC_IN_SV_6
A13	PD1_UART4_TX	PE8_UART7_TX
A14	PB5_FDCAN2_RX	PE9_UART7_RTS
A15	PD11_SAI2_SD_A	PD13_SAI2_SCK_A
A16	PF14_I2C1_SCL	PF15_I2C1_SDA
A17	GND_2	GND_8
A18	VCC_LD06	GND_9
A19	PA7_ETH1_RX_CTL	PC5_ETH1_RXD1
A20	PC2_ETH1_TXD2	PB12_ETH1_TXD0
A21	PD14_ETH1_TXD1	PA1_ETH1_RX_CLK
A21	PE2_ETH1_TXD3	PA2_ETH1_MDIO
B1		GND_10
B2	GND_3	VCC_BUCK4_3
B3	VCC_IO_3	VCC_BUCK4_4
B4	VCC_IO_4	GND_11
B5	PA12_FDCAN1_TX	GND_12
B6	VBUS_SW_2	USBH_HS1_DM
B7	GND_4	GND_13
B8	VBUS_OTG_2	PA10_USB_OTG_HS_ID
B9	PB6_QSPI_BK1_CS#	USB_OTG_HS_DM
B10	PF7_QSPI_BK1_I02	PF10_QSPI_BK1_CLK
B11	VCC_IN_SV_3	VCC_IN_SV_7
B12	VCC_IN_SV_4	VCC_IN_SV_8
B13	PB2_UART4_RX	PE10_UART7_CTS
B14	PB13_FDCAN2_TX	PE7_UART7_RX
B15	PF11_SAI2_SD_B	PD12_SAI2_FS_A
B16	PC0_SAI2_FS_B	PH2_SAI2_SCK_B
B17	PF12_ADC1_INP6	GND_13
B17	PF13_ADC2_INP2	GND_14
B18	PG11_ETH1_TX_CTL	PH6_ETH1_RXD2
B19	PC4_ETH1_RXD0	PB1_ETH1_RXD3
B20	PC1_ETH1_MDC	PG5_ETH1_CLK125
B21	PG4_ETH1_GTX_CLK	VCC_LD02

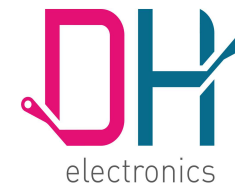
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X2B		
E1	B00T2	PZ7_HWC00
E2	B00T1	PG0_RAMC00
E3	B00T0	PB11
E4	PONKEY#	PG1_RAMC01
F1	PMIC_WAKEUP	PC3
F2	VBA1	PG2_CSI_ERROR
F3	PA0_SYS_WKUP1	PF3_HWC01
F4	ANA1	PA14
G1		PI5_SD_SEL
G2	RST#	PD8
G3	PA13_RCC_MCO_1	PD8
G4	NC_1	PD14
G4	ANA0	PG3_CSI_INT
H1	NC_2	H18
H2	NC_3	PZ3_WIFI_LED
H3	NC_4	PD15
H4	NC_29	PZ6_BT_LED
J1	NC_5	PZ1_ETH1_INT
J2	NC_6	PZ2_ETH1_RST
J3	NC_7	PI8_SD_CD
J4	NC_30	PG9_HDMI_INT
K1	NC_8	PF1_I2C2_SCL
K2	NC_9	PH5_I2C2_SDA
K3	NC_10	P20_CSI_XSDN
K4	NC_31	PZ5_I2C4_SDA
L1	NC_11	PZ4_I2C4_SCL
L2	NC_12	PD3_USART2_CTS
L3	NC_13	PF5_USART2_TX
L4	NC_32	PD4_USART2_RTS
M1	NC_14	PD6_USART2_RX
M2	NC_15	PB10_SPI2_SCK
M3	NC_16	PI3_SPI2_MOSI
M4	NC_33	PI2_SPI2_MISO
N1	NC_17	PI0_SPI2_NSS
N2	NC_18	GND_21
N3	NC_19	GND_18
N4	NC_34	GND_19
P1	NC_20	GND_20
P2	NC_21	NC_36
P3	NC_22	NC_26
P4	NC_35	NC_27
R1	GND_15	NC_28
R2	GND_16	PG10_LTDC_B2
R3	GND_17	PB0_LTDC_G1
R4	GND_22	PA3_LTDC_B5
T1	NC_23	PG13_LTDC_R0
T2	NC_24	PG7_LTDC_CLK
T3	NC_25	PG8_LTDC_G7
T4	SYS_JTRST	PH3_LTDC_R1
U1	SYS_JTD0-SWO	PI10_LTDC_HSYNC
U2	SYS_JTD1	PB8_LTDC_B6
U3	SYS_JTCK-SWCLK	PI11_LTDC_G6
U4	SYS_JTMS-SWDIO	PA5_LTDC_R4
		PD9_LTDC_B0

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X2C		
V1	GND_26	NC_43
V2	NC	NC_44
V3	DSI_CK_P	DSI_D1_N
V4	DSI_D0_P	PC11_SDMMC1_D3
V5	PC2_SDMMC1_CMD	PC9_SDMMC1_D1
V6	PC12_SDMMC1_CK	PB9_SDMMC1_CD1R
V7	NC_51	GND_23
V8	NC_50	GND_24
V9	PC6_SDMMC2_D6	PA8_SDMMC2_D4
V10	PB14_SDMMC2_D0	PC7_SDMMC2_D7
V12	PD7_SDMMC3_D3	PD0_SDMMC3_CMD
V13	PF0_SDMMC3_D0	PE3_SDMMC2_CK
V14	NC_52	NC_45
V15	PA6_DCM1_PIXCLK	NC_46
V16	PH7_DCM1_D9	PI4_DCM1_D5
V17	PE6_DCM1_D7	PE0_DCM1_D2
V18	PI6_DCM1_D6	PH14_DCM1_D4
V19	PG12_LTDC_B1	PH4_LTDC_G5
V20	PI9_LTDC_VSYNC	PH11_LTDC_R5
V21	PE12_LTDC_B4	PH12_LTDC_R6
V21	PI7_LTDC_B7	PH15_LTDC_G4
W1	NC_37	NC_47
W2	NC_38	NC_48
W3	DSI_CK_N	DSI_D1_P
W4	DSI_D0_N	PE4_SDMMC1_CKIN
W5	PC10_SDMMC1_D2	PF2_SDMMC1_D0DIR
W6	PC8_SDMMC1_D0	PE14_SDMMC1_D123DIR
W7	NC_39	VCC_SD_LD05_1
W8	NC_40	VCC_SD_LD05_2
W9	PB3_SDMMC2_D2	PG6_SDMMC2_CMD
W10	PB15_SDMMC2_D1	PB4_SDMMC2_D3
W11	PA15_SDMMC2_D5	PG15_SDMMC3_CK
W12	PF4_SDMMC3_D1	PD5_SDMMC3_D2
W13	NC_41	GND_25
W14	NC_42	NC_49
W15	PA4_DCM1_HSYNC	PB7_DCM1_VSYNC
W16	PE1_DCM1_D3	PA9_DCM1_D0
W17	PI1_DCM1_D8	PH10_DCM1_D1
W18	PH9_LTDC_R3	PE5_LTDC_G0
W19	PH8_LTDC_R2	PD10_LTDC_B3
W20	PE15_LTDC_R7	PH11_LTDC_G3
W21	PH13_LTDC_G2	PE13_LTDC_DE

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X1A			
A2	A2	C1	C2
A3	A3	C2	C3
A4	A4	C3	C4
A5	A5	C4	C5
A6	A6	C5	C6
A7	A7	C6	C7
A8	A8	C7	C8
A9	A9	C8	C9
A10	A10	C9	C10
A11	A11	C10	C11
A12	A12	C11	C12
A13	A13	C12	C13
A14	A14	C13	C14
A15	A15	C14	C15
A16	A16	C15	C16
A17	A17	C16	C17
A18	A18	C17	C18
A19	A19	C18	C19
A20	A20	C19	C20
A21	A21	C20	C21
	A21	C21	
B1	B1	D1	D1
B2	B2	D2	D2
B3	B3	D3	D3
B4	B4	D4	D4
B5	B5	D5	D5
B6	B6	D6	D6
B7	B7	D7	D7
B8	B8	D8	D8
B9	B9	D9	D9
B10	B10	D10	D10
B11	B11	D11	D11
B12	B12	D12	D12
B13	B13	D13	D13
B14	B14	D14	D14
B15	B15	D15	D15
B16	B16	D16	D16
B17	B17	D17	D17
B18	B18	D18	D18
B19	B19	D19	D19
B20	B20	D20	D20
B21	B21	D21	D21

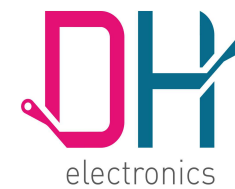
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X1B			
E1	E1	E18	E18
E2	E2	E19	E19
E3	E3	E20	E20
E4	E4	E21	E21
F1	F1	F18	F18
F2	F2	F19	F19
F3	F3	F20	F20
F4	F4	F21	F21
G1	G1	G18	G18
G2	G2	G19	G19
G3	G3	G20	G20
G4	G4	G21	G21
H1	H1	H18	H18
H2	H2	H19	H19
H3	H3	H20	H20
H4	H4	H21	H21
J1	J1	J18	J18
J2	J2	J19	J19
J3	J3	J20	J20
J4	J4	J21	J21
K1	K1	K18	K18
K2	K2	K19	K19
K3	K3	K20	K20
K4	K4	K21	K21
L1	L1	L18	L18
L2	L2	L19	L19
L3	L3	L20	L20
L4	L4	L21	L21
M1	M1	M18	M18
M2	M2	M19	M19
M3	M3	M20	M20
M4	M4	M21	M21
N1	N1	N18	N18
N2	N2	N19	N19
N3	N3	N20	N20
N4	N4	N21	N21
P1	P1	P18	P18
P2	P2	P19	P19
P3	P3	P20	P20
P4	P4	P21	P21
R1	R1	R18	R18
R2	R2	R19	R19
R3	R3	R20	R20
R4	R4	R21	R21
T1	T1	T18	T18
T2	T2	T19	T19
T3	T3	T20	T20
T4	T4	T21	T21
U1	U1	U18	U18
U2	U2	U19	U19
U3	U3	U20	U20
U4	U4	U21	U21

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X1C			
V1	V1	Y1	Y1
V2	V2	Y2	Y2
V3	V3	Y3	Y3
V4	V4	Y4	Y4
V5	V5	Y5	Y5
V6	V6	Y6	Y6
V7	V7	Y7	Y7
V8	V8	Y8	Y8
V9	V9	Y9	Y9
V10	V10	Y10	Y10
V11	V11	Y11	Y11
V12	V12	Y12	Y12
V13	V13	Y13	Y13
V14	V14	Y14	Y14
V15	V15	Y15	Y15
V16	V16	Y16	Y16
V17	V17	Y17	Y17
V18	V18	Y18	Y18
V19	V19	Y19	Y19
V20	V20	Y20	Y20
V21	V21	Y21	Y21
W1	W1	AA1	AA1
W2	W2	AA2	AA2
W3	W3	AA3	AA3
W4	W4	AA4	AA4
W5	W5	AA5	AA5
W6	W6	AA6	AA6
W7	W7	AA7	AA7
W8	W8	AA8	AA8
W9	W9	AA9	AA9
W10	W10	AA10	AA10
W11	W11	AA11	AA11
W12	W12	AA12	AA12
W13	W13	AA13	AA13
W14	W14	AA14	AA14
W15	W15	AA15	AA15
W16	W16	AA16	AA16
W17	W17	AA17	AA17
W18	W18	AA18	AA18
W19	W19	AA19	AA19
W20	W20	AA20	AA20
W21	W21	AA21	AA21

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X3A		
A2	VCC_IO_1	GND_5
A3	VCC_IO_2	VCC_BUCK4_1
A4	PA11	VCC_BUCK4_2
A5	VBUS_SW_1	GND_6
A6	GND_1	USBH_HS1_DP
A7	VBUS_OTG_1	GND_7
A8	PF9_QSPI_BK1_I01	USB_OTG_HS_VBUS
A9	PF8_QSPI_BK1_I00	USB_OTG_HS_DP
A10	VCC_IN_5V_1	PF6_QSPI_BK1_I03
A11	VCC_IN_5V_2	VCC_IN_5V_5
A12	PD1	VCC_IN_5V_6
A13	PD1	PE8
A14	PD1	PE9
A15	PD11	PD13
A16	PD11	PF15
A17	GND_2	GND_8
A18	VCC_LD06	GND_9
A19	PA7	PC5
A20	PC2	PB12
A21	PG14	PA1
PE2		PA2
B1	GND_3	GND_10
B2	VCC_IO_3	VCC_BUCK4_3
B3	VCC_IO_4	VCC_BUCK4_4
B4	PA12	GND_11
B5	VBUS_SW_2	USBH_HS1_DM
B6	GND_4	GND_12
B7	VBUS_OTG_2	PA10
B8	PF6_QSPI_BK1_CS#	USB_OTG_HS_DM
B9	PF7_QSPI_BK1_I02	PF10_QSPI_BK1_CLK
B10	VCC_IN_5V_3	VCC_IN_5V_7
B11	VCC_IN_5V_4	VCC_IN_5V_8
B12	PD2	PE10
B13	PD13	PE7
B14	PD11	PD12
B15	PC0	PH2
B16	PF12	GND_13
B17	PF13	GND_14
B18	PG11	PH6
B19	PC4	PB1
B20	PC1	PG5
B21	PG4	VCC_LD02

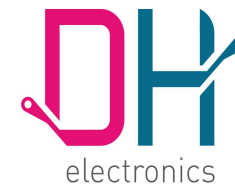
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X3B		
E1	B00T2	P77
E2	B00T1	P60
E3	B00T0	PB11
E4	PONKEY#	P61
F1	PMIC_WAKEUP	PC3
F2	VBA1T	F19
F3	PA0_SYS_WKUP1	PG2
F4	ANA1	PF3
G1	RST#	PA14
G2	PA13	P15
G3	NC_1	PD8
G4	ANA0	PD14
H1	NC_2	PG3
H2	NC_3	H18
H3	NC_4	P23
H4	NC_29	H19
J1	NC_5	PD15
J2	NC_6	P26
J3	NC_7	H21
J4	NC_30	P21
K1	NC_8	J18
K2	NC_9	J19
K3	NC_10	P18
K4	NC_31	J20
L1	NC_11	PG9
L2	NC_12	PF1
L3	NC_13	K18
L4	NC_32	K19
M1	NC_14	PH5
M2	NC_15	P20
M3	NC_16	K20
M4	NC_33	K21
N1	NC_17	L18
N2	NC_18	PD3
N3	NC_19	PF5
N4	NC_34	L20
P1	NC_20	PD4
P2	NC_21	L21
P3	NC_22	M18
P4	NC_35	M19
R1	GND_15	PB10
R2	GND_16	P13
R3	GND_17	P12
R4	GND_22	P10
T1	NC_23	GND_21
T2	NC_24	GND_18
T3	NC_25	GND_19
T4	SYS_JTRST	GND_20
U1	SYS_JTD0-SWO	NC_36
U2	SYS_JTD1	NC_26
U3	SYS_JTCK-SWCLK	NC_27
U4	SYS_JTMS-SWDIO	NC_28
		NC_37
		NC_38
		DSI_D1_P
		DSI_D0_N
		PC10
		PC8
		NC_39
		NC_40
		PG6
		PB3
		PB15
		PA15
		PF4
		NC_41
		NC_42
		PA4
		PE1
		PH1
		PH9
		PH8
		PE11
		PH13
		U18
		U19
		U20
		U21

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X3C		
V1	GND_26	NC_43
V2	NC	NC_44
V3	DSI_CK_P	DSI_D1_N
V4	DSI_D0_P	PC11
V5	PD2	PC9
V6	PC12	PB9
V7	NC_50	GND_23
V8	NC_51	GND_24
V9	PC6	PA8
V10	PB14	PC7
V11	PD7	PD0
V12	PF0	PE3
V13	NC_52	NC_45
V14	PA6	NC_46
V15	PH7	PH4
V16	PE6	PH6
V17	PH6	PH14
V18	PG12	PH4
V19	PH9	PH11
V20	PE12	PH12
V21	PH7	PH15
W1	NC_37	NC_47
W2	NC_38	NC_48
W3	DSI_CK_N	DSI_D1_P
W4	DSI_D0_N	PE4
W5	PC10	PF2
W6	PC8	PE14
W7	NC_39	VCC_SD_LD05_1
W8	NC_40	VCC_SD_LD05_2
W9	PG6	AA10
W10	PB3	PB4
W11	PB15	PG15
W12	PA15	PD5
W13	NC_41	GND_25
W14	NC_42	NC_49
W15	PA4	PB7
W16	PE1	PA9
W17	PH1	PH10
W18	PH9	PE5
W19	PH8	PD10
W20	PE11	PE11
W21	PH13	PE13
		AA1
		AA2
		AA3
		AA4
		AA5
		AA6
		AA7
		AA8
		AA9
		AA10
		AA11
		AA12
		AA13
		AA14
		AA15
		AA16
		AA17
		AA18
		AA19
		AA20
		AA21

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