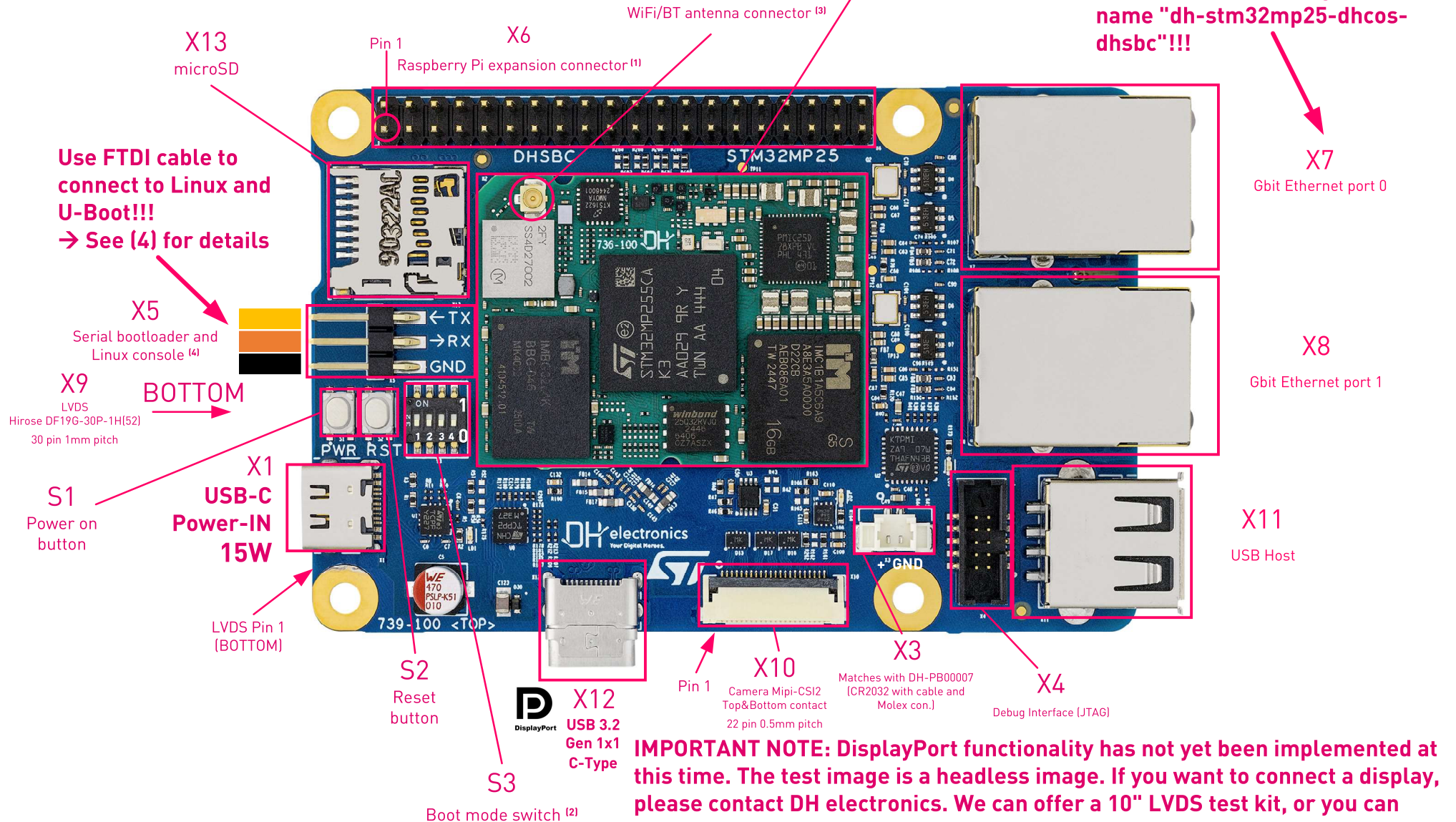


DHSBC STM32MP25 Quick-Start-Guide



(1) Pin assignment

Pin	DHC05	MP2 signal	MP25 port	Pin	DHC05	MP2 signal	MP25 port
1	VIO	VDD_3V3	-	2	VCC_IN	VDD_5V	-
3	I2C0_SDA	I2C2_SDA	PB4	4	VCC_IN	VDD_5V	-
5	I2C0_SCL	I2C2_SCL	PB5	6	GND	GND	-
7	#INT_HIGH_PRI0	GPIO	PI8	8	UART1_TX	UART4_TX	PB7
9	GND	GND	-	10	UART1_RX	UART4_RX	PB6
11	UART1_RTS	UART4_RTS	PD9	12	I2S_TXC	SAI2_SCKB	PA9
13	GPIO_B (BL_EN)	GPIO	PZ3	14	GND	GND	-
15	UART2_TX	UART9_TX	PG8	16	CAN0_TX	FDCAN1_TX	PB9
17	VIO	VDD_3V3	-	18	CAN0_RX	FDCAN1_RX	PB11
19	SPI_MOSI/(QSPI_D0)	SPI8_MOSI/(OCTOSPIM_P2_I00)	PZ7/(PB0)	20	GND	GND	-
21	SPI_MISO/(QSPI_D1)	SPI8_MISO/(OCTOSPIM_P2_I01)	PZ8/(PB1)	22	CAN1_TX	FDCAN3_TX	PD2
23	SPI_SCLK/(QSPI_SCLK)	SPI8_SCK/(OCTOSPIM_P2_CLK)	PZ5/(PB10)	24	SPI_SS/(QSPI_SS)	SPI8_NSS/(OCTOSPIM_P2_NCS1)	PZ6/(PB8)
25	GND	GND	-	26	CAN1_RX	FDCAN3_RX	PD1
27	QSPI_D2	GPIO	PB2	28	UART2_RX	UART9_RX	PI5
29	QSPI_D3	GPIO	PB3	30	GND	GND	-
31	UART2_RTS	UART9_RTS	PF12	32	PWM0	TIM5_CH2	PH4
33	PWM1	TIM8_CH2	PZ1	34	GND	GND	-
35	I2S_TXFS	SAI2_FSB	PA6	36	UART1_CTS	UART4_CTS	PD8
37	UART2_CTS	UART9_CTS	PG7	38	I2S_RXD	SAI2_SDA	PG1
39	GND	GND	-	40	I2S_TXD	SAI2_SDB	PA10

(2)

B00T3 S3 / 4	B00T2 S3 / 3	B00T1 S3 / 2	B00T0 S3 / 1	Initial boot mode	Comments
0	0	0	0	UART and USB	Wait incoming connection on: – USART6 = DHC05 UART0 – USB high-speed device on USB3DR_DP/DM = DHC05 USB0
0	0	0	1	SD card	SDMMC1 = DHC05 SDI00
0	0	1	0	eMMC	SDMMC2 = eMMC located on System-On-Module
0	0	1	1	Development boot (no flash boot)	Used to get debug access without boot from flash memory
0	1	0	0	Serial NOR-Flash	OCTOSPIM_P1 = Quad SPI located on System-On-Module

For all other boot mode options, please refer to the STM32MP2 datasheet

(3)

On the DHC05 STM32MP25 a standard U.FL. connector from TE 1909763-1 is used. U.FL. is compatible with the IPEX MHF1 connector specification. DH recommends the following antenna as a possible external antenna for the DHC05 STM32MP25:
Unictron AA258 - H2B1PC1A1C095L

(4)

Please use the FTDI TTL-232R-RPI cable for PC connection.
Needed Terminal configuration:
Baud rate: 115200, Data: 8 bit, Parity: none, Stop: 1 bit, Flow control: none
Matches with DH-KR00026