

DHCOS STM32MP255C HS00043 DHCS-STM32MP255C-C120-R409-EE-F1638-SPI04-RTC-WBT-USB3-I-01LG

please contact DH electronics. We can offer a 10" LVDS test kit, or you can

connect your own LVDS display and we will assist with implementation.

Pre-installed Linux obtains an DHSBC STM32MP25 Quick-Start-Guide IP address via DHCP. You can access the board using the host WiFi/BT antenna connector (3) name "dh-stm32mp25-dhcos-X6 X13 dhsbc"!!! Raspberry Pi expansion connector (1) microSD Use FTDI cable to STM32MP25 connect to Linux and Gbit Ethernet port 0 U-Boot!!! → See (4) for details X5 Serial bootloader and X8 Linux console (4) GND X9 Gbit Ethernet port 1 **BOTTOM** LVDS Hirose DF19G-30P-1H(52) 30 pin 1mm pitch X1 **USB-C S1** X11 Power-IN Power on electronics button 15W **USB Host** LVDS Pin 1 (BOTTOM) **S2** Matches with DH-PB00007 Χ4 Camera Mipi-CSI2 (CR2032 with cable and X12 Reset Top&Bottom contact Debug Interface (JTAG) button **USB 3.2** 22 pin 0.5mm pitch Gen 1x1 IMPORTANT NOTE: DisplayPort functionality has not yet been implemented at C-Type this time. The test image is a headless image. If you want to connect a display,

Boot mode switch (2)

(1) Pin assignment

Pin	DHCOS	MP2 signal	MP25 port	Pin	DHCOS	MP2 signal	MP25 port
1	VIO	VDD_3V3	-	2	VCC_IN	VDD_5V	-
3	I2C0_SDA	I2C2_SDA	PB4	4	VCC_IN	VDD_5V	-
5	I2C0_SCL	I2C2_SCL	PB5	6	GND	GND	-
7	#INT_HIGH_PRIO	GPI0	PI8	8	UART1_TX	UART4_TX	PB7
9	GND	GND	-	10	UART1_RX	UART4_RX	PB6
11	UART1_RTS	UART4_RTS	PD9	12	I2S_TXC	SAI2_SCKB	PA9
13	GPIO_B (BL_EN)	GPI0	PZ3	14	GND	GND	-
15	UART2_TX	UART9_TX	PG8	16	CAN0_TX	FDCAN1_TX	PB9
17	VIO	VDD_3V3	-	18	CAN0_RX	FDCAN1_RX	PB11
19	SPI_MOSI/(QSPI_D0)	SPI8_MOSI/(OCTOSPIM_P2_I00)	PZ7/(PB0)	20	GND	GND	-
21	SPI_MISO/(QSPI_D1)	SPI8_MISO/(OCTOSPIM_P2_I01)	PZ8/(PB1)	22	CAN1_TX	FDCAN3_TX	PD2
23	SPI_SCLK/(QSPI_SCLK)	SPI8_SCK/(OCTOSPIM_P2_CLK)	PZ5/(PB10)	24	SPI_SS/(QSPI_SS)	SPI8_NSS/(OCTOSPIM_P2_NCS1)	PZ6/(PB8)
25	GND	GND	-	26	CAN1_RX	FDCAN3_RX	PD1
27	QSPI_D2	GPI0	PB2	28	UART2_RX	UART9_RX	PI5
29	QSPI_D3	GPI0	PB3	30	GND	GND	-
31	UART2_RTS	UART9_RTS	PF12	32	PWM0	TIM5_CH2	PH4
33	PWM1	TIM8_CH2	PZ1	34	GND	GND	-
35	I2S_TXFS	SAI2_FSB	PA6	36	UART1_CTS	UART4_CTS	PD8
37	UART2_CTS	UART9_CTS	PG7	38	I2S_RXD	SAI2_SDA	PG1
39	GND	GND	-	40	I2S_TXD	SAI2_SDB	PA10

(2)

300T3	B00T2	B00T1	воото	Initial boot mode	Comments		
					Wait incoming connection on:		
					- USART6 = DHCOS UART0		
0	0	0	0	UART and USB	USB high-speed device on USB3DR_DP/DM = DHCOS USB0		
0	0	0	1	SD card	SDMMC1 = DHCOS SDI00		
0	0	1	0	eMMC	SDMMC2 = eMMC located on System-On-Module		
0	0	1	1	Development boot (no flash boot)	Used to get debug access without boot from flash memory		
0	1	0	0	Serial NOR-Flash	OCTOSPIM P1 = Quad SPI located on System-On-Module		

(3)

On the DHCOS STM32MP25 a standard U.FL. connector from TE 1909763-1 is used. U.FL. is compatible with the IPEX MHF1 connector specification. DH recommends the following antenna as a possible external antenna for the DHCOS STM32MP25:
Unictron AA258 - H2B1PC1A1C095L

(4)

Please use the FTDI TTL-232R-RPI cable for PC connection.

Needed Terminal configuration:

Baud rate: 115200, Data: 8 bit, Parity: none, Stop: 1 bit, Flow control: none

Matches with DH-KR00026